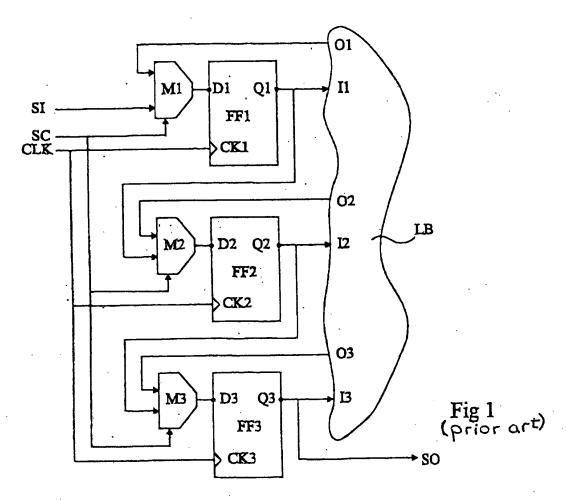
Replacement Sheet
INTEGRATED CIRCUIT TESTING METHOD AND SYSTEM
Jacques PRUNIER
Serial No.: 10/083,714
Docket No.: \$1022.80854U\$00

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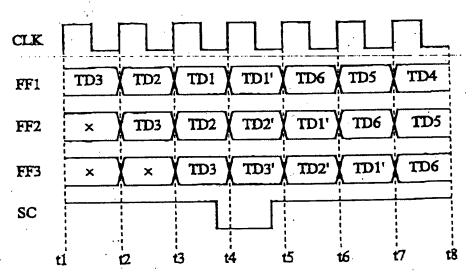


Fig 2 (prior art)

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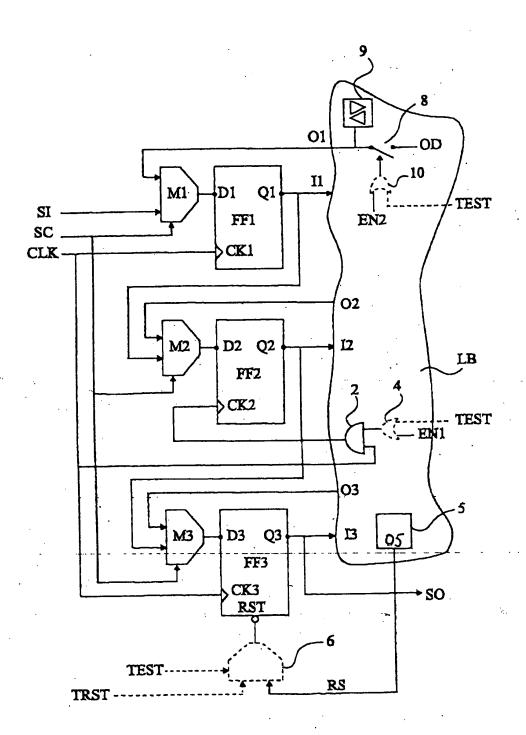


Fig 3 (prior art)

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